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PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Roy E. Scheuerlein, et al.

Title: MULTIPLE TWIN CELL NON-VOLATILE MEMORY ARRAY AND  
LOGIC BLOCK STRUCTURE AND METHOD THEREFOR

Application No.: 10/675,212

Filed: September 30, 2003

Examiner: Ho, Hoai V.

Group Art Unit: 2827

Atty. Docket No.: 023-0024

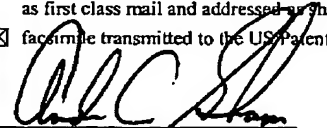
Confirmation No.: 9946

March 8, 2006

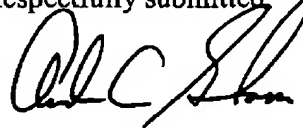
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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**NOTICE OF APPEAL FROM THE EXAMINER TO THE BOARD OF  
PATENT APPEALS AND INTERFERENCES**

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the last decision of the Examiner.

The fee for this Notice of Appeal under 37 C.F.R. § 41.20(b)(1) is submitted as set forth in the accompanying transmittal letter.

<b>CERTIFICATE OF MAILING OR TRANSMISSION</b>	
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 Andrew C. Graham	<u>3-8-06</u> Date

Respectfully submitted

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